



Sheet 1 of 1

FORM PTO-1449

ATTY. DOCKET NO.
I-2-0183.1USSERIAL NO.
10/046,601U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEAPPLICANT
Edward L. HeplerINFORMATION DISCLOSURE
STATEMENT BY APPLICANTFILING DATE
October 23, 2001GROUP
2131

(Use several sheets if necessary)

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CA	AB	5	3	1	1	1	7	6	05/1994	Gurney			
CA	AC	5	6	0	2	8	3	3	02/1997	Zehavi			
CA	AD	5	7	5	1	7	6	1	05/1998	Gilhoussen			
CA	AE	6	0	1	4	4	0	8	01/2000	Naruse et al.			
CA	AF	6	0	9	1	7	5	7	06/2000	Cudak et al.			
CA	AG	6	1	1	5	4	1	0	09/2000	Naruse			

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

3/23/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

jc821 U.S. PTO
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[illegible][illegible]

EAT	AA		Kung, VLSI Array Processors, pgs. 436-438, 1988

EXAMINER <i>Camp Arundel</i>	DATE CONSIDERED 3/23/05
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